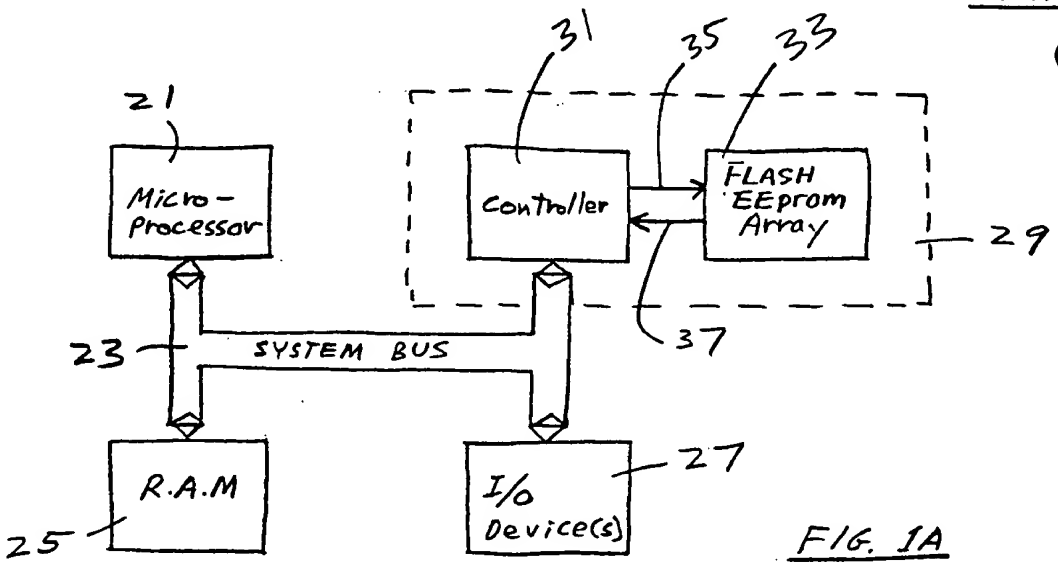

21
Micro-Processor
31
Controller
35
33
FLASH EEprom Array
29
23
SYSTEM BUS
37
R.A.M
25
I/O Device(s)
27

FIG. 1A

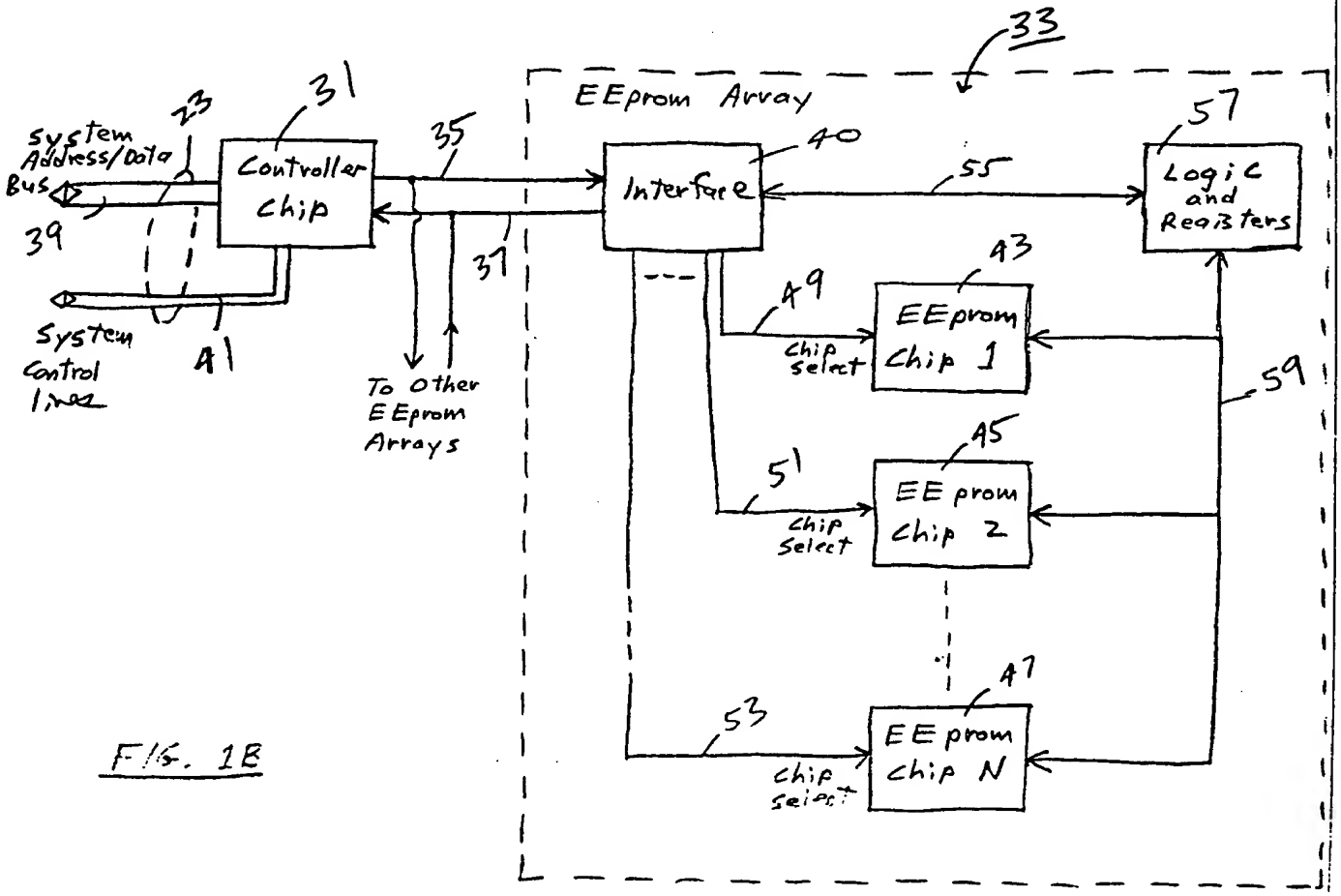

33
EEprom Array
System Address/Data Bus
23
39
Controller Chip
31
35
Interface
40
55
Logic and Registers
57
41
System Control lines
43
EEprom Chip 1
49
chip select
59
To other EEprom Arrays
37
45
EEprom Chip 2
51
chip select
47
EEprom Chip N
53
chip select

FIG. 1B

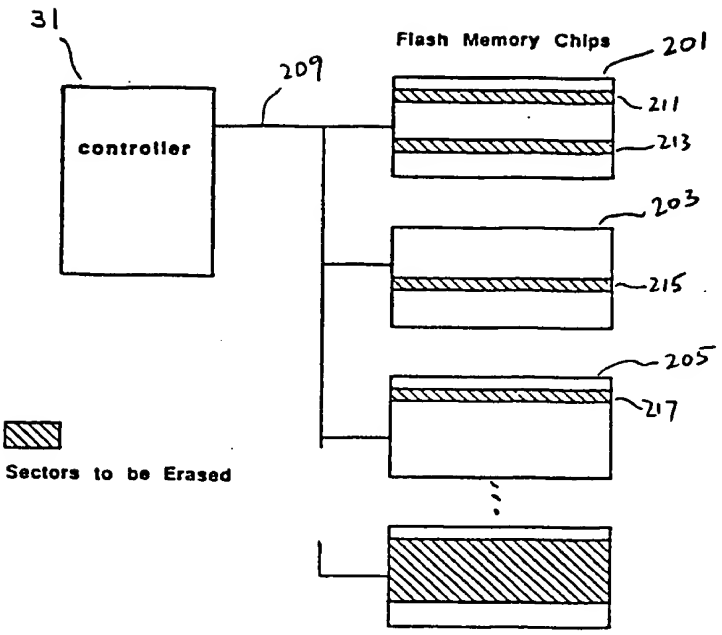


FIG_2

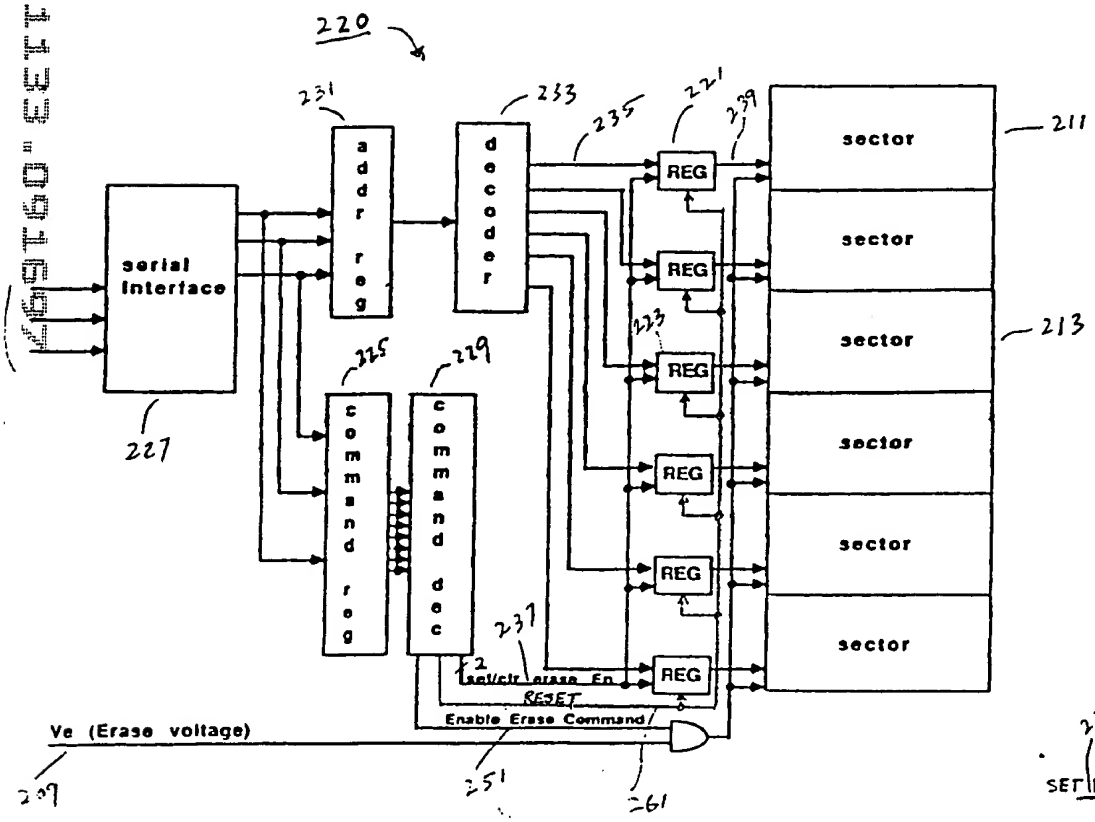


FIG_3A

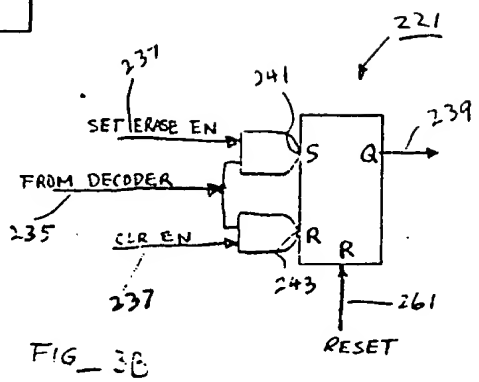


FIG_3B

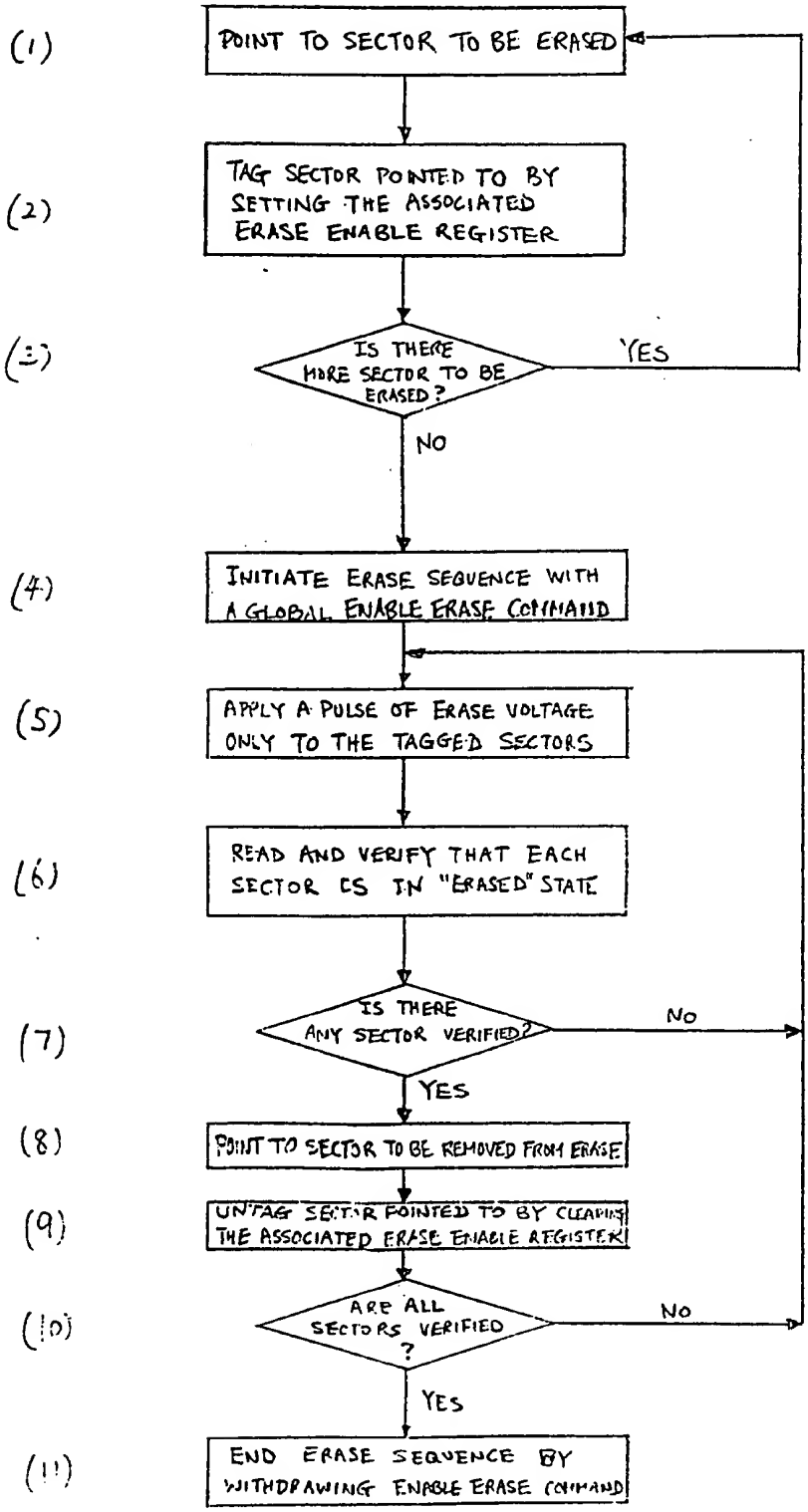
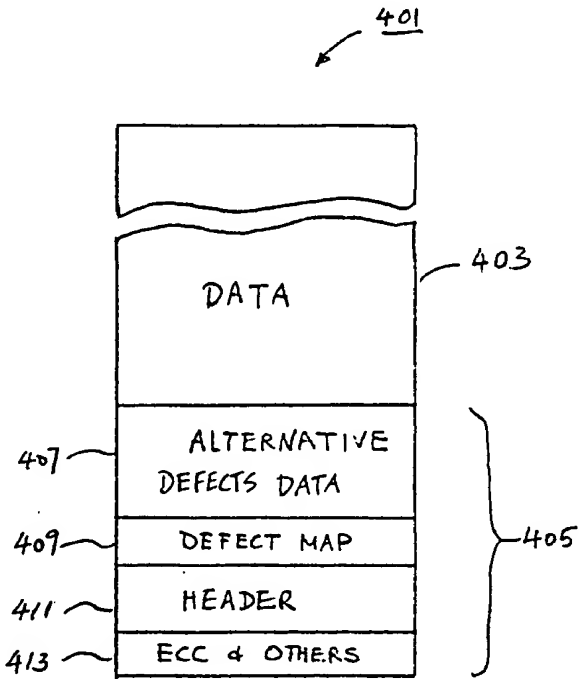


FIG_4

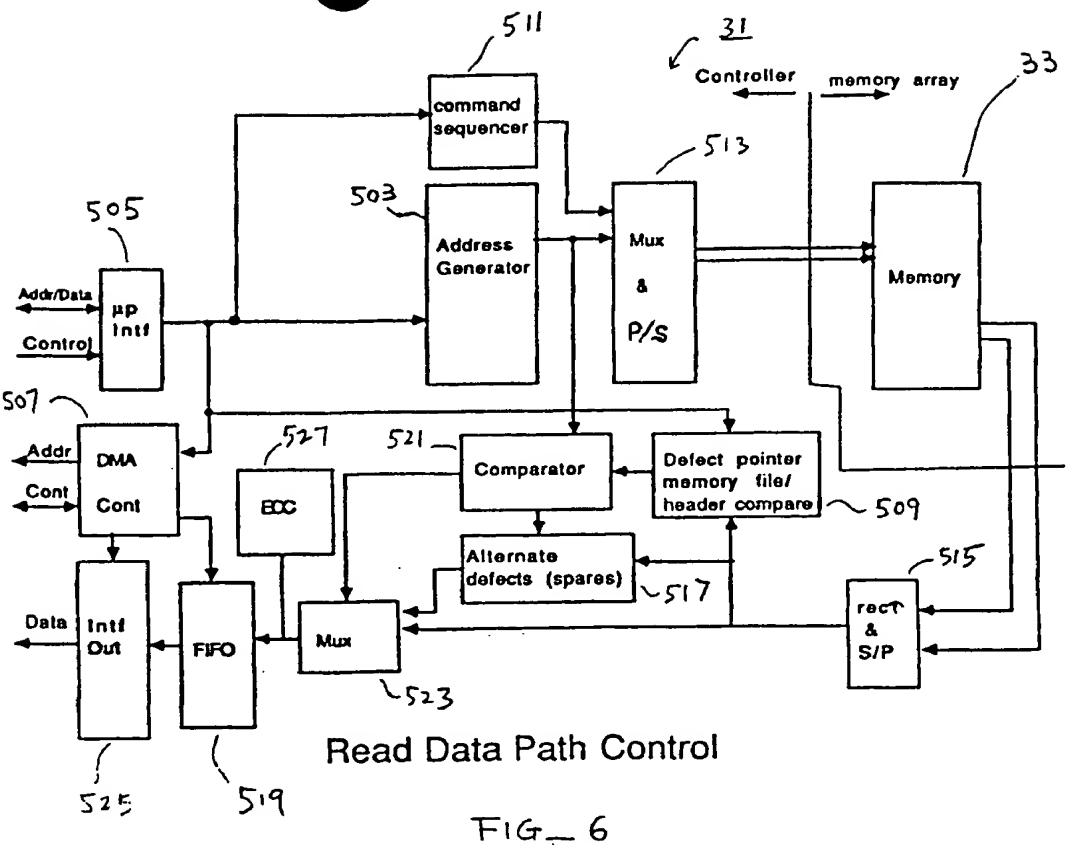
5,959,929



SECTOR PARTITION

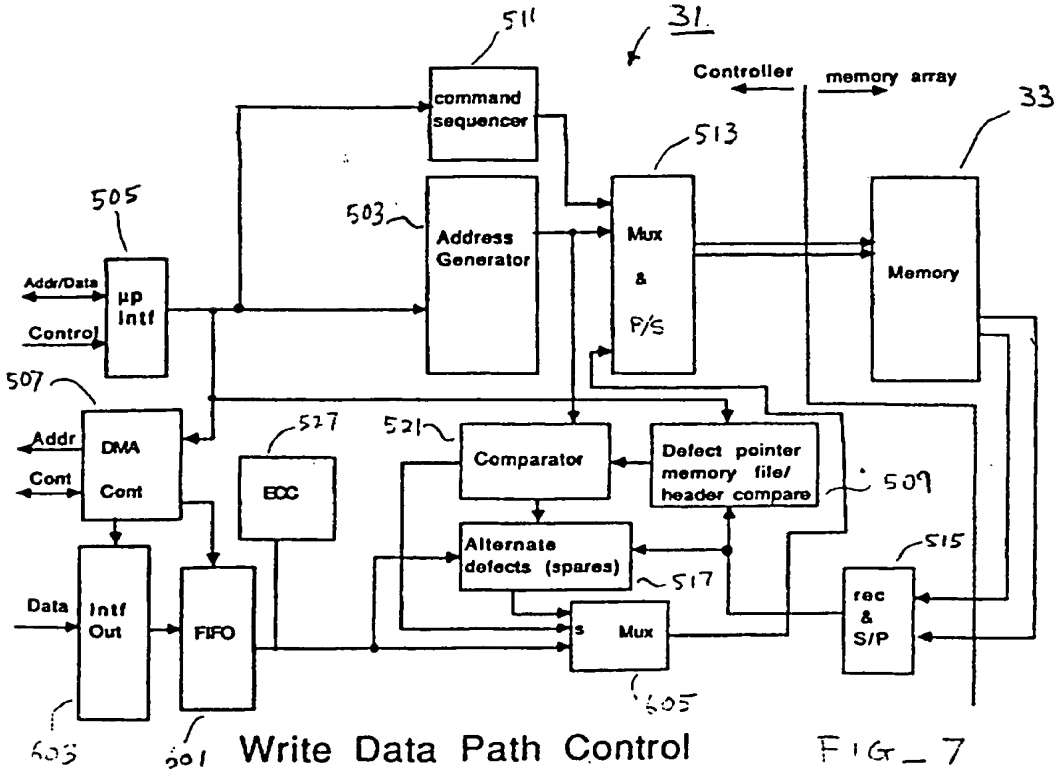
FIG_5

5,913,039



Read Data Path Control

FIG_6



Write Data Path Control

FIG_7

2907650 "EETES69

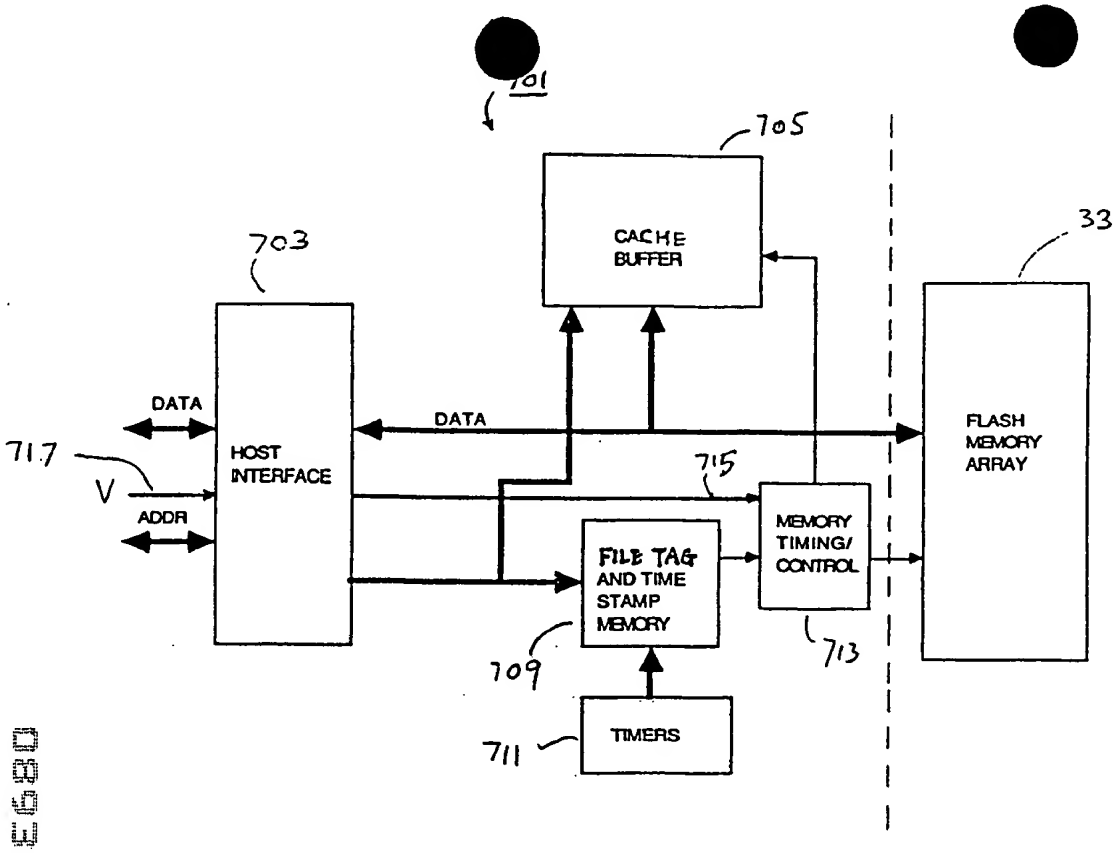


FIG. 8

08/31/98 15:59 0